



## PCIe 6.0, CXL 3.0 Next-Gen Retimer

Phison PS7262 is an 8-lane bidirectional retimer IC designed for PCIe 6.0 and CXL 3.0 applications supporting up to 64 Gbps data rate. The solution extends the reach and robustness of PCIe serial links over motherboards, interconnect cables, and more complex multi-connector topologies. In addition, PS7262 supports diagnostic features like lane margining, eye opening, thermal monitoring, and error indication to get users dialed in on enterprise and server deployment scenarios.



### Application

Server / NAS / HPC

PC Motherboard / Gaming Console

Hardware Accelerator

### Product Features

#### High Range of EQ Boosting with Low Latency

PS7262 offers an high range of EQ boosting up to 32dB. In addition to the EQ boosting capability is the <10ns low latency that enables uninterrupted signal transmissions at all times.

#### Diagnostic Tool and BMC SDK

PS7262 comes with diagnostic tools that support per-link diagnostic information such as link history and other electrical statistics through SMBus access. In addition, the support of standard C BMC SDK enables a wide range for functions like firmware updating and link status monitoring.

#### Flexible Bifurcation and Hot Plug Support

Link bifurcation is supported in 1x8, 2x4, and other configurations. With the hot-plug-supported independent links, PS7262 enables worry-free, flexible deployments with the freedom of device removal during system uptimes.

#### PCI-SIG-compliant PCIe Retimers

The PCI-SIG compliance workshop provides extensive interoperability testing with diverse root complexes and endpoints for the verification of PCIe signal conditioning solutions. Passing the PCI-SIG compliance test demonstrates both the quality and excellence in design of Phison's most advanced solution, PS7262.

# Retimer

PS7262

Features	Specifications
Interface	<ul style="list-style-type: none"><li>- PCIe 6.0</li><li>- Backward compatible with existing PCIe generation transfer rates</li><li>- CXL 3.0 Support</li></ul>
Lane Count	<ul style="list-style-type: none"><li>- 8 lanes</li></ul>
Function	<ul style="list-style-type: none"><li>- Retimer</li></ul>
Package	<ul style="list-style-type: none"><li>- FCCSP 211</li></ul>
Body Size	<ul style="list-style-type: none"><li>- 13.2 mm x 8.9mm</li><li>(Compatible with Intel supplemental Retimer footprint)</li></ul>
Supply Voltage	<ul style="list-style-type: none"><li>- 0.85V, 1.2V</li></ul>
Max EQ Boosting Range	<ul style="list-style-type: none"><li>- 32dB @ 16GHz</li></ul>
Operating Junction Temperature	<ul style="list-style-type: none"><li>- Operating range: -10 ~ 110 °C</li><li>- Storage range: -10 ~ 110 °C</li></ul>
PCIe Protocol Participation	<ul style="list-style-type: none"><li>- Protocol aware</li></ul>
Embedded AC Capacitors	<ul style="list-style-type: none"><li>- Support</li></ul>
BMC SDK	<ul style="list-style-type: none"><li>- Support standard C-SDK</li></ul>
Diagnostic Tool	<ul style="list-style-type: none"><li>- Support LTSSM state and history, lane margin</li></ul>



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