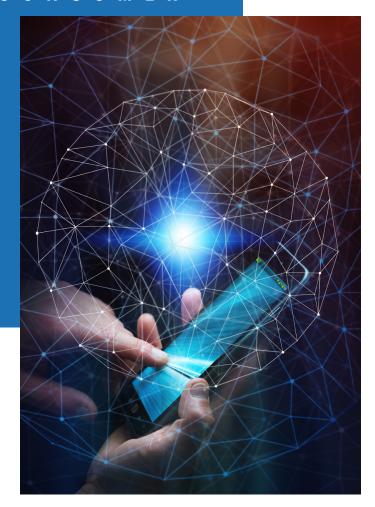


CONSUMER



Breaking Ground with UFS Storage

Revolutionize your mobile solution design with Phison's PS8327 UFS solution that brings next-level transmission performance with the UFS 2.2 high speed interface. The PS8327 is the perfect storage upgrade from eMMCs in commercial electronics designs. Get one step ahead and make the swap to unleash your 5G smartphones as well as cutting-edge mobile devices with GB-scale storage performance and bandwidth.



PS8327 - Starting Changes with UFS 2.2

PS8327, Phison's cutting-edge UFS 2.2 solution, enables capacity up to 256GB with low power consumption needs. Quick-firing data signals at 1600 MT/s, the PS8325 also provides GB-scale sequential read performance with excellent random operation capabilities.

Now equipped with Phison's 6th Gen LDPC engine (4KB codewords), which offers stronger soft-bit decoding than previous generations for improved correction strength, the PS8327 provides comprehensive support for requirements that come in state-of-the-art 3D TLC and QLC NAND flash technology.

SOLUTIONS

PS8327

	Consoit: (1)	6400	12000	0F60D
Capacity (1)		64GB	128GB	256GB
Interface		HS-G3x2 UFS 2.2		
Form Factor		UFS		
Package		153 Ball (11.5 mm x 13 mm x 0.8 mm)		
NAND Flash		3D TLC (160+ Layers)		
		Performance (esti	mation)	
Sequential Read		1000 MB/s	1000 MB/s	1000 MB/s
Sequential Write		530 MB/s	950 MB/s	950 MB/s
4K Random Read		60K IOPS	100K IOPS	110K IOPS
4K Random Write		75K IOPS	130K IOPS	160K IOPS
		Power Consumption (estimation)	
Supply Voltage	VCC	3.3 V	3.3 V	3.3 V
	VCCQ	1.8 V	1.8 V	1.8 V
Active (Average)	ICC	85 mA	125 mA	125 mA
	ICCQ	340 mA	340 mA	340 mA
		Environmental Chara	acteristics	
Operating Temperature		-25°C ~ 85°C		
Storage Temperature		-40°C ~ 85°C		
		Features		

- Write booster
- Permanent and power-on write protection
- Command queuing
- Background operation
- 2-lanes supported

(1) 1 GB = 1,000,000,000 bytes



THE DATA WITHIN THIS SPECIFICATION IS SUBJECT TO CHANGE BY PHISON WITHOUT NOTICE. PERFORMANCE NUMBERS MAY VARY BASED ON SYSTEM CONFIGURATION AND TESTING CONDITIONS.

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